

## QUANTUM LOGIC GATES: A DETAILED THEORETICAL STUDY OF THE QUANTUM NOT GATE

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### ABSTRACT

Quantum computers are meant to perform quantum computations. In order to understand how quantum computers perform quantum computations, we need to understand qubits' quantum states and how we can change those states. Quantum logic gates help do just that – change qubits' quantum states so that quantum computers can perform the desired quantum computations. This review paper is aimed at performing a detailed theoretical study of the Quantum NOT Gate.

**KEYWORDS:** Quantum Computing, Quantum Computer, Qubit, Quantum Bit, Quantum Logic Gate, Quantum NOT Gate.

### I. INTRODUCTION

Quantum logic gates serve as the basic building blocks for quantum computers to perform quantum computations. Quantum logic gates manipulate quantum information by changing the quantum state(s) of a qubit or a collection of them. Just as classical computers employ the use of classical logic gates such as the AND, OR, NOT gates and the likes in order to perform different types of computations, quantum computers use quantum logic gates in order to perform quantum information processing tasks and quantum computations.

### II. ANALYSIS

#### a) Quantum NOT Gate: Algebraic Representation

Our first topic of discussion, the Quantum NOT Gate, is a generalization of the classical NOT Gate used in classical computers. For the computational basis states  $|0\rangle$  and  $|1\rangle$ , the quantum NOT gate simply takes the state  $|0\rangle$  as the input and produces the state  $|1\rangle$  as the output; similarly, the state  $|1\rangle$  as the input to the quantum NOT gate produces the state  $|0\rangle$  as the output. The notation  $X$  is used as the general notation for representing a quantum NOT gate.

$$\text{So, } X |0\rangle = |1\rangle$$

$$X |1\rangle = |0\rangle$$

When the quantum NOT gate is applied to a general quantum superposition state  $\alpha |0\rangle + \beta |1\rangle$  of a qubit, the quantum NOT gate acts linearly on the general quantum superposition state  $\alpha |0\rangle + \beta |1\rangle$  and interchanges the  $|0\rangle$  and  $|1\rangle$  states.

$$X (\alpha |0\rangle + \beta |1\rangle) = \alpha |1\rangle + \beta |0\rangle$$

#### b) Quantum NOT Gate: Quantum Circuit Representation

The Quantum Circuit representation of the  $X$  gate looks like the following.

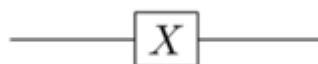


Fig-1: Quantum Circuit representation of X Gate.

It is also a common convention to explicitly specify the Input and Output quantum states in a Quantum Circuit, as depicted in the following figure.

$$\alpha|0\rangle + \beta|1\rangle \text{ --- } \boxed{X} \text{ --- } \alpha|1\rangle + \beta|0\rangle$$

**Fig-2:** Quantum Circuit representation of X Gate with the specified Input and Output quantum states.

**NOTE: -**

If you take a closer look at the Quantum Circuit representation depicted in Figure 2 above, the straight line that passes from left to right through the quantum NOT gate X is called a Quantum Wire and it represents a single qubit. The term ‘Quantum Wire’, the way it has been drawn in the quantum circuit representation and the way the overall quantum circuit itself is read or interpreted, it seems as if the qubit is moving from left to right through space. But that is not how the quantum circuit should be interpreted – instead, the quantum wire’s left-to-right representation should be thought of as a representation of the passage of time. Thus, the segment of the quantum wire that sits to the left of the quantum NOT gate X should be strictly interpreted as representing only the passage of time and nothing is happening to the qubit as such. The quantum NOT gate X then gets applied to the qubit’s input state. And, finally, the segment of the quantum wire that sits to the right of the quantum NOT gate X leads to the desired output state. In fact, the quantum circuit representation of the quantum NOT gate X clearly depicts that we have performed a quantum computation involving a single qubit and quantum logic gate.

**c) Quantum NOT Gate: Matrix Representation**

The following denotes a 2x2 matrix representation of a quantum NOT gate.

$$X = \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}$$

**Fig-3:** 2x2 Matrix Representation of the Quantum NOT Gate

Now, let us prove that the 2x2 matrix representation depicted in figure 3 above is indeed that of the quantum NOT gate. For that, let us take into consideration the general quantum superposition state  $\alpha|0\rangle + \beta|1\rangle$ , and note down the vector notations of the computational basis states  $|0\rangle$  and  $|1\rangle$ .

It is known that

$$|0\rangle = \begin{bmatrix} 1 \\ 0 \end{bmatrix}$$

$$|1\rangle = \begin{bmatrix} 0 \\ 1 \end{bmatrix}$$

**Fig-4:** Vector notations of the computational basis states  $|0\rangle$  and  $|1\rangle$

Therefore,

$$\begin{aligned}
 & \alpha|0\rangle + \beta|1\rangle \\
 &= \alpha \begin{bmatrix} 1 \\ 0 \end{bmatrix} + \beta \begin{bmatrix} 0 \\ 1 \end{bmatrix} \\
 &= \begin{bmatrix} \alpha \\ 0 \end{bmatrix} + \begin{bmatrix} 0 \\ \beta \end{bmatrix} \\
 &= \begin{bmatrix} \alpha \\ \beta \end{bmatrix}
 \end{aligned}$$

**Fig-5:** General Quantum Superposition State  $\alpha|0\rangle + \beta|1\rangle$  represented in a vector notation

So, we now have

$$\begin{aligned}
 & \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix} (\alpha|0\rangle + \beta|1\rangle) \\
 &= \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix} \begin{bmatrix} \alpha \\ \beta \end{bmatrix} \\
 &= \begin{bmatrix} 0 \times \alpha + 1 \times \beta \\ 1 \times \alpha + 0 \times \beta \end{bmatrix} \\
 &= \begin{bmatrix} \beta \\ \alpha \end{bmatrix} \\
 &= \beta|0\rangle + \alpha|1\rangle \\
 &= \alpha|1\rangle + \beta|0\rangle \\
 &= X (\alpha|0\rangle + \beta|1\rangle)
 \end{aligned}$$

**Fig-6:** Mathematical proof of the 2x2 matrix representation of a quantum NOT gate

Therefore, it has been established that

$$X = \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}$$

**d) Simplest Quantum Circuit**

By now, you might be tempted enough to believe that the Quantum NOT gate X could be the simplest possible quantum circuit. But that is not the case. In fact, the simplest quantum circuit is a single quantum wire which does absolutely nothing. A single quantum wire quantum circuit is merely a representation of a single qubit being preserved in time. In other words, an arbitrary qubit quantum state  $|\psi\rangle$  (Greek alphabet, Psi), when input to a single quantum wire quantum circuit, outputs the same arbitrary qubit quantum state  $|\psi\rangle$ .



**Fig-7:** The simplest Quantum Circuit – a single Quantum Wire

**e) Multi X gate Quantum Circuit**

The following figure depicts a simple multi X gate quantum circuit that consists of two X gates in a row.



**Fig-8:** A multi X gate Quantum Circuit, comprising of two X gates in a row

Let us consider the general quantum superposition state  $\alpha |0\rangle + \beta |1\rangle$  being input to the multi X gate quantum circuit depicted in figure 8 above.

$$\begin{aligned} & X(X(\alpha |0\rangle + \beta |1\rangle)) \\ &= X(\alpha |1\rangle + \beta |0\rangle) \\ &= \alpha |0\rangle + \beta |1\rangle \end{aligned}$$

We can clearly see that a multi X gate quantum circuit, comprising of two X gates in a row, as depicted in figure 8 above, produces as output the same general quantum superposition state  $\alpha |0\rangle + \beta |1\rangle$  that it had previously taken as its input. A multi X gate quantum circuit, comprising of two X gates in a row, as depicted in figure 8 above, thus, recovers the original quantum state and is therefore equivalent to a single quantum wire quantum circuit.



**Fig-9:** A multi X gate Quantum Circuit, comprising of two X gates in a row, as a Quantum Circuit is equivalent to a single quantum wire quantum circuit

If we input an arbitrary qubit quantum state  $|\psi\rangle$  to the multi X gate Quantum Circuit, comprising of two X gates in a row, as depicted in figure 8, the first X gate outputs the quantum state  $X|\psi\rangle$ . The quantum state  $X|\psi\rangle$  is now the input to the second X gate, which outputs the quantum state  $XX|\psi\rangle$ .

Now,

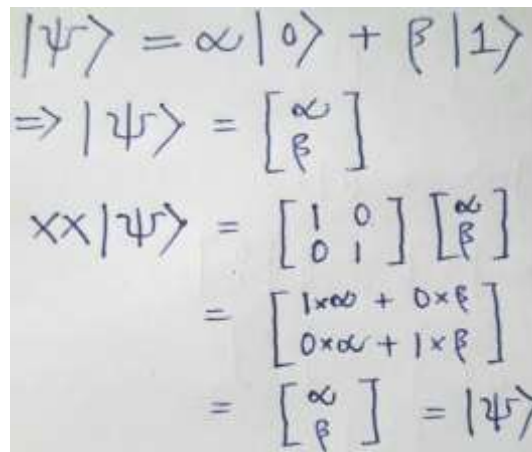
$$\begin{aligned} XX &= \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix} \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix} \\ &= \begin{bmatrix} 0 \times 0 + 1 \times 1 & 0 \times 1 + 1 \times 0 \\ 1 \times 0 + 0 \times 1 & 1 \times 1 + 0 \times 0 \end{bmatrix} \\ &= \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix} \end{aligned}$$

**Fig-10:** Product of XX

Therefore, the product of XX is an Identity Matrix. By the property of the identity matrix operation,  $XX|\psi\rangle = |\psi\rangle$ .

Let us assume that  $|\psi\rangle = \alpha |0\rangle + \beta |1\rangle$

Then,



$$\begin{aligned}
 |\psi\rangle &= \alpha|0\rangle + \beta|1\rangle \\
 \Rightarrow |\psi\rangle &= \begin{bmatrix} \alpha \\ \beta \end{bmatrix} \\
 XX|\psi\rangle &= \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix} \begin{bmatrix} \alpha \\ \beta \end{bmatrix} \\
 &= \begin{bmatrix} 1 \times \alpha + 0 \times \beta \\ 0 \times \alpha + 1 \times \beta \end{bmatrix} \\
 &= \begin{bmatrix} \alpha \\ \beta \end{bmatrix} = |\psi\rangle
 \end{aligned}$$

**Fig-11:** Mathematically proving that  $XX|\psi\rangle = |\psi\rangle$

Thus, we have again proved that a multi X gate quantum circuit, comprising of two X gates in a row, as depicted in figure 8 above, is equivalent to a single quantum wire quantum circuit.

### III. CONCLUSION

This review paper provides the reader a detailed perspective of one of the simpler quantum logic gates – the quantum NOT gate X, which, we have seen is a generalization of the classical NOT gate used in the classical computers.

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### IV. REFERENCES

- [1] Abramsky S.; Duncan R. (2006). A categorical quantum logic – Mathematical Structures in Computer Science, Volume 16, Issue 3; Cambridge University Press, (pp.469 – 489).
- [2] Acín A. (2001). Statistical distinguishability between unitary operations – Physical Review Letters, 87, art. no. 177901; American Physical Society, (<https://doi.org/10.1103/PhysRevLett.87.177901>).
- [3] Aerts D.; Czachor M. (2004). Quantum aspects of semantic analysis and symbolic artificial intelligence, Volume 37, Number 12; Journal of Physics A: Mathematical and General, (pp. L123-L132).
- [4] Aïmeur E.; Brassard G.; Gambs S. (2006). Machine learning in a quantum world, vol. 4013; Conference of the Canadian Society for Computational Studies of Intelligence - Proceedings of Canadian AI 2006, (pp. 431-442).
- [5] Altenkirch T.; Grattage J. (2005). A functional quantum programming language; Proceedings of the 20th Annual IEEE Symposium on Logic in Computer Science (LICS), (pp. 249–258).
- [6] Andresen S.L. (2002). John McCarthy: Father of AI; IEEE Intelligent Systems, (pp. 84-85).

- [7] Baltag A.; Smets S. (2006). LQP: The dynamic logic of quantum information - Mathematical Structures in Computer Science, Volume 16, Issue 3; Cambridge University Press, (pp. 491-525).
- [8] Barenco A.; Bennet C.; Cleve R.; DiVincenzo D.P.; Margolus N.; Shor P.; Sleator T.; Smolin J.A.; Weinfurter H. (1995). Elementary gates for quantum computation, Physical Review A 52, 3457; American Physical Society, (pp. 3457-3467).
- [9] Benioff P.A. (1980). The computer as a physical system: A microscopic quantum mechanical Hamiltonian model of computers as represented by Turing machines; Journal of Statistical Physics, 22, (pp. 563-591).
- [10] Abramsky S. (2004). High-level methods for quantum computation and information; Proceedings of the 19th Annual IEEE Symposium on Logic in Computer Science, (pp. 410-414).
- [11] Abramsky S.; Coecke B. (2004). A categorical semantics of quantum protocols; Proceedings of the 19th Annual IEEE Symposium on Logic in Computer Science, (pp. 415-425).
- [12] Benjamin S.C.; Hayden P.M. (2001). Multiplayer quantum games, Physical Review A, 64, 030301(R); American Physical Society, (art. no. 030301).
- [13] Bennet C.H. (1973). Logical reversibility of computation; IBM Journal of Research and Development, 17, (pp. 525-532)
- [14] Bernstein E.; Vazirani U. (1997). Quantum complexity theory; SIAM Journal on Computing, 26, (pp. 1411-1473)
- [15] Bertoni A.; Mereghetti C.; Palano B. (2003). Quantum computing: 1-way quantum automata; Proceedings of 7th International Conference on Developments in Language Theory, LNCS, vol. 2710, Springer, (pp. 1-20).